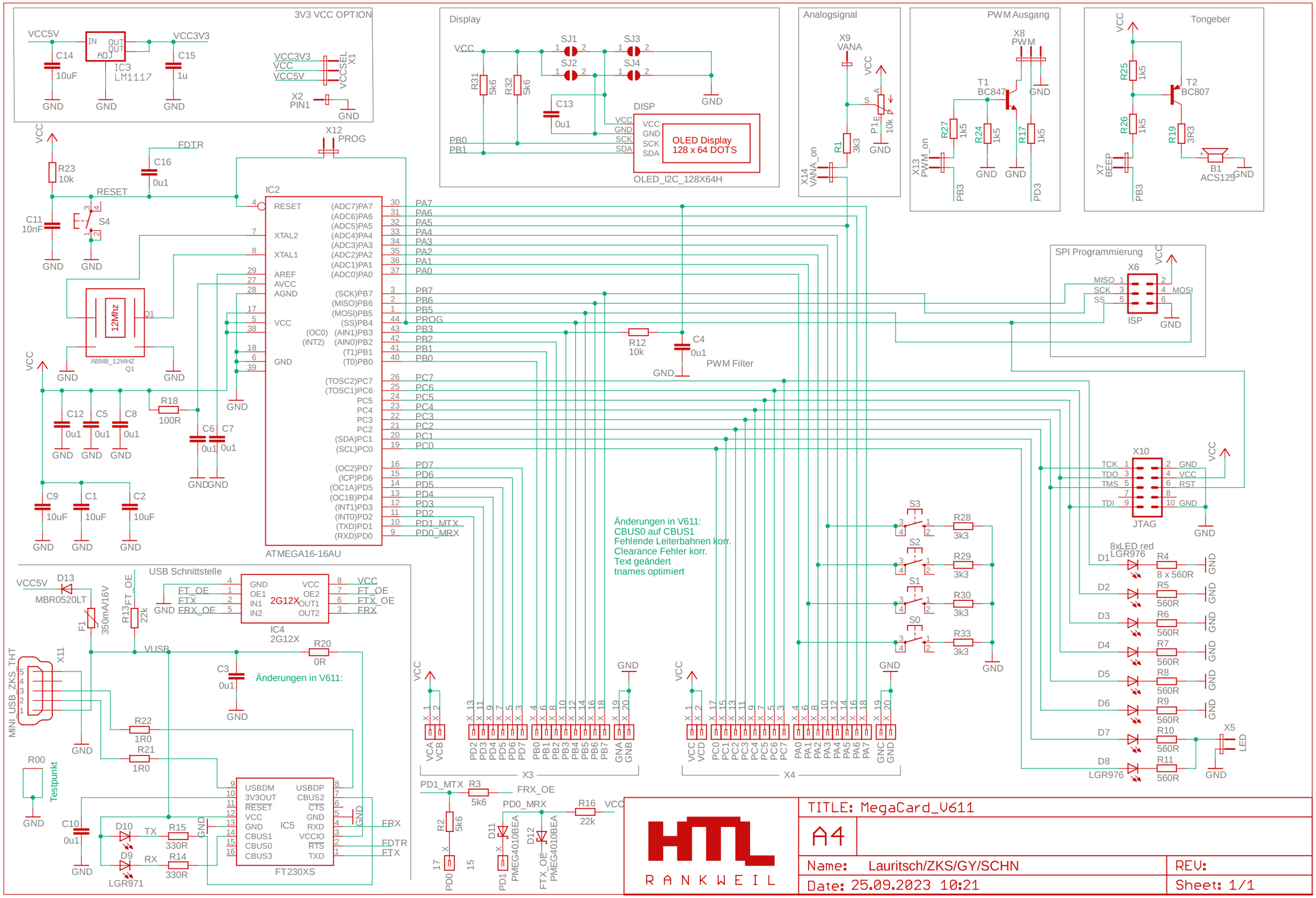
3V3 VCC OPTION
VCC5V
VCC3V3
IC3
LM1117
C14
10uF
C15
1u
X1
VCCSEL
X2
PIN1
Display
SJ1
SJ2
SJ3
SJ4
R31
5k6
R32
5k6
C13
0u1
DISP
OLED Display
128 x 64 DOTS
OLED_I2C_128X64H
Analogsignal
X9
VANA
R1
3k3
P1
10k
X14
VANA_on
PWM Ausgang
X8
PWM
T1
BC847
R27
1k5
R24
1k5
R17
1k5
X13
PWM_on
Tongeber
R25
1k5
R26
1k5
T2
BC807
R19
3R3
B1
ACS125
X7
BEEP
X12
PROG
FDTR
C16
0u1
R23
10k
RESET
S4
C11
10nF
IC2
ATMEGA16-16AU
12Mhz
Q1
ABM8_12MHZ
R18
100R
C12
C5
C8
C6
C7
C9
10uF
C1
C2
R12
10k
C4
0u1
PWM Filter
SPI Programmierung
X6
ISP
MISO
SCK
SS
MOSI
X10
JTAG
TCK
TDO
TMS
TDI
S3
S2
S1
S0
R28
R29
R30
R33
3k3
8xLED red
LGR976
R4
8 x 560R
R5
R6
R7
R8
R9
R10
R11
560R
X5
LED
Änderungen in V611:
CBUS0 auf CBUS1
Fehlende Leiterbahnen korr.
Clearance Fehler korr.
Text geändert
tnames optimiert
USB Schnittstelle
VCC5V
D13
MBR0520LT
F1
350mA/16V
R13
22k
FT_OE
2G12X
IC4
2G12X
X11
MINI_USB_ZKS_THT
VUSB
R20
0R
C3
0u1
Änderungen in V611:
R22
1R0
R21
1R0
R00
Testpunkt
C10
0u1
D10
D9
TX
RX
R15
330R
R14
330R
LGR971
IC5
FT230XS
FRX
FDTR
FTX
X3
X4
PD1_MTX
R3
5k6
FRX_OE
PD0_MRX
R16
22k
R2
5k6
D11
D12
PMEG4010BEA
HTL
RANKWEIL
TITLE: MegaCard_V611
A4
Name: Lauritsch/ZKS/GY/SCHN
REV:
Date: 25.09.2023 10:21
Sheet: 1/1